

FIG. 1

Field	Byte Length	Byte Offset	Description
Header			
Signature	4	0	'CPEP' Signature for the Corrected Platform Error Polling Table.
Length	4	4	Length, in bytes, of the entire Corrected Platform Error Polling Table
Revision	1	8	1
Checksum	1	9	Entire table must sum to zero.
OEMID	6	10	OEM ID
OEM Table ID	8	16	For the Corrected Platform Error Polling Table, the table ID is the manufacturer model ID.
OEM Revision	4	24	OEM revision of Corrected Platform Error Polling Table for supplied OEM Table ID.
Creator ID	4	28	Vendor ID of utility that created the table. For tables containing Definition Blocks, this is the ID for the ASL Compiler.
Creator Revision	4	32	Revision of utility that created the table. For tables containing Definition Blocks, this is the revision for the ASL Compiler.
Reserved	8	36	Reserved, must be 0.
CPEP Structure[n]	---	44	A list of CPEP structures for the platform. The CPEP structure is defined in the following section.

FIG. 2.

Field	Byte Length	Byte Offset	Description
Type	1	0	0—Corrected Platform Error Polling structure
Length	1	1	8
Processor ID	1	2	Processor ID of destination.
Processor EID	1	3	Processor EID of destination.
Polling Interval	4	4	Platform-suggested polling interval (in milliseconds)

FIG. 3

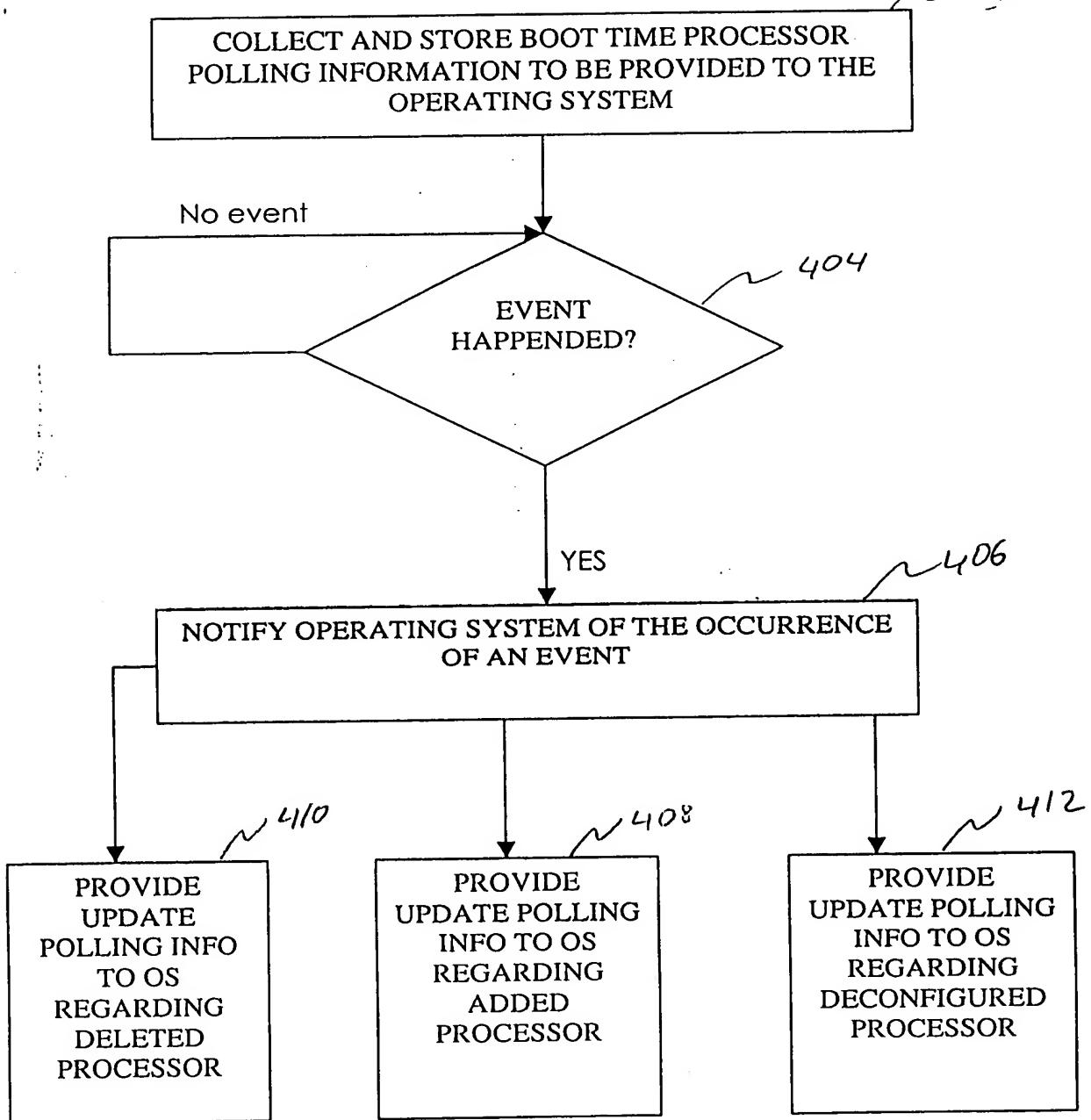


FIG. 4

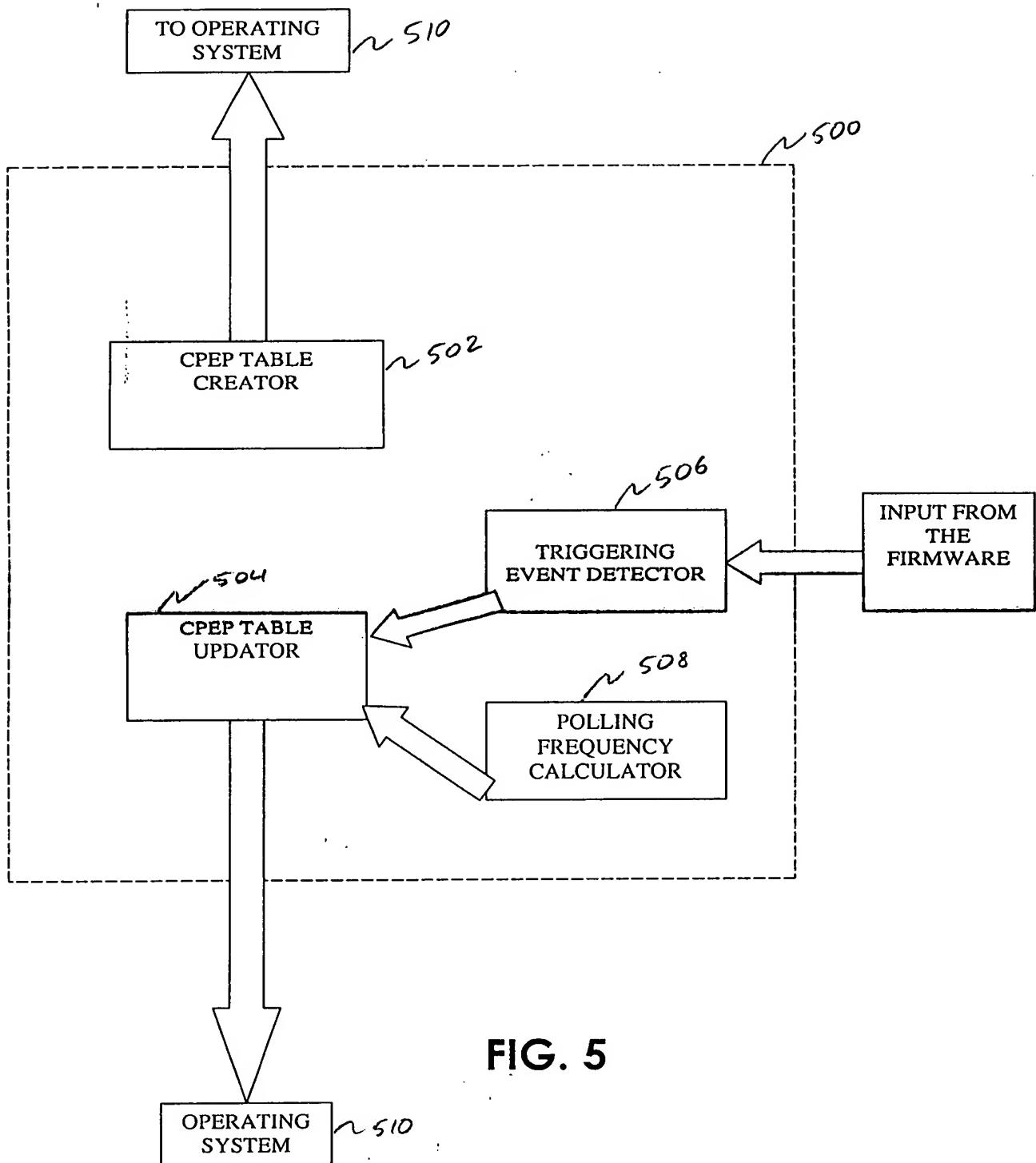


FIG. 5